

PATENTS ABSTRACTS OF JAPAN

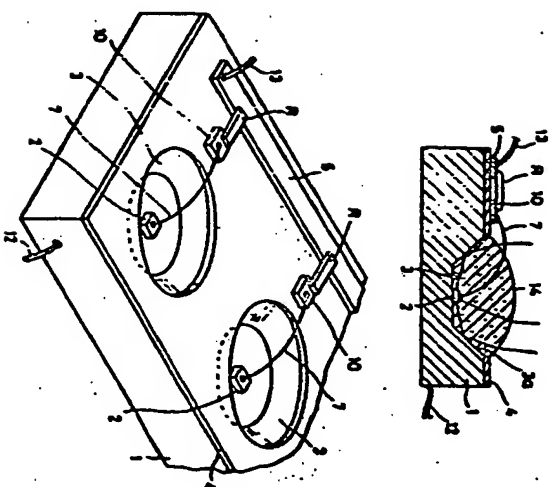
E-582 Feb. Jary 13, 1988 Vol. 12/No. 49

(54) ILLUMINATION SYSTEM

- (11) 62-196878 (A) (43) 31.8.1987 (19) JP  
 (21) Appl. No. 61-38177 (22) 25.2.1986  
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 (51) Int. Cl. H01L33/00, F21Q1/00//F21V19/00

**PURPOSE:** To readily manufacture an illuminator by directly arranging a light emitting element in an element containing recess to conduct it with a metal substrate, thereby eliminating the necessity of laminating an insulating layer and a conductive layer in the recess.

**CONSTITUTION:** A plurality of element containing recesses 3 are formed at a suitable interval longitudinally on a metal substrate 1 made of a conductive material, and the surface except the recesses 3 is covered with an insulating layer 4. A common conductive layer 5 is so formed on the layer 4 as to extend longitudinally of the substrate 1. A light emitting element 2 is made of a semiconductor chip, directly disposed fixedly at the center on the bottom of each recess 3 to conduct with the substrate 1, and electrically connected via gold wirings 7 as leads with the conductive layer 10 of the element.



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99 P 77 71

LEGENDE

zu den Bibliographiedaten

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| (54) Titel der Patentanmeldung         | (22) Anmeldetag in Japan                 |
| (11) Nummer der JP-A2 Veröffentlichung | (71) Anmelder                            |
| (21) Aktenzeichen der JP-Anmeldung     | (72) Erfinder                            |
| (43) Veröffentlichungstag              | (52) Japanische Patentklassifikation     |
|  | (51) Internationale Patentklassifikation |

H01L, 27/15